



Docket No.: 32014-141666

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Sakaino et al.

Application No.: 09/229,628

Art Unit: N/A

Filed: January 13, 1999

Examiner: Not Yet Assigned

For: SEMICONDUCTOR INTEGRATED CIRCUIT

## **AMENDMENT**

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

## INTRODUCTORY COMMENTS

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 14 of this paper.

